

**What is claimed is:**

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5 1. A thin film transistor substrate in a liquid crystal display provided with a data lines for applying a data signals, a gate line for applying a gate signal, and a pixel electrode for driving a liquid crystal cell, said substrate comprising:

10 a gate dummy pattern formed in such a manner to be extended in the vertical direction from the gate line and to overlap with the data line and the pixel electrode.

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15 2. The thin film transistor substrate according to claim 1, wherein the gate dummy pattern is formed in such a manner to overlap with one side or both sides of the data line and the edge of the pixel electrode adjacent thereto.

20 3. The thin film transistor substrate according to claim 2, wherein the gate dummy pattern is used as a redundancy electrode for electrically connecting the gate line to the broken data line.

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25 4. The thin film transistor substrate according to claim 3, wherein the gate dummy pattern includes a hole connected to the gate line and formed to permit a repair.

5. The thin film transistor substrate according to claim 1, wherein the gate dummy pattern is used as a black matrix.

30 6. The thin film transistor substrate according to claim 1, further comprising:

a storage capacitor defined by a horizontal overlapping part between the gate line and the pixel

electrode.

7. The thin film transistor substrate according to claim 4, further comprising:

- 5 a protrusion formed in such a manner to overlap with the hole, thereby shutting off a light leaked between the gate dummy pattern and the gate line.

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